Programme Educational Objectives (PEOs)

- **PEO 1**
    - To impart fundamental principles and design skills in integrated circuits, systems and signal processing.
- **PEO 2**
    - To train students in using contemporary industry grade electronic design automation tools for VLSI design
- **PEO 3**
    - To expose students to latest developments in VLSI Design for pursuing research, formulating solutions to contemporary research problems of societal relevance.
- **PEO 4**
    - To instil teamwork, leadership, and communication skills in the student with professional, ethical, and human values to be responsible citizen of the society