

# **GITAM UNIVERSITY**

(Declared as Deemed to be University U/S 3 of UGC Act, 1956)



**Scheme of Instruction  
OF  
M.Tech. (Evening) (VLSI Design)**  
(w.e.f 2009 -10 admitted batch)

Gandhi Nagar Campus, Rushikonda

**VISAKHAPATNAM – 530 045**

Website: [www.gitam.edu](http://www.gitam.edu)

**M.Tech (Evening)(VLSI Design)**  
**Programme Code: EPEVD 200801**

**I Semester**

Course Code	Name of the Course	Credits	Hours per Week	Continuous Evaluation	Semester End Examination	Total Marks
EPEVD101	Semiconductor Device Modeling	4	4L	40	60	100
EPEVD102	Digital IC Design	4	4L	40	60	100
EPEVD103	Digital Logic Design	4	4L	40	60	100
EPEVD104	Analog IC Design	4	4L	40	60	100
EPEVD111	HDL Programming and EDA Tools Laboratory	2	4P	100	-	100
	Total	18	20	260	240	500

**II semester**

Course Code	Name of the Course	Credits	Hours per Week	Continuous Evaluation	Semester End Examination	Total Marks
EPEVD201	Analog System Design	4	4L	40	60	100
EPEVD202	Advanced Digital IC Design	4	4L	40	60	100
EPEVD203	VLSI Physical Design	4	4L	40	60	100
EPEVD204	Digital Systems Testing and Testability	4	4L	40	60	100
EPEVD211	VLSI Design Laboratory	2	4P	100	-	100
EPEVD212	Comprehensive Viva Voce	2	--	--	100	100
	Total	20	20	260	340	600

## M.Tech (Evening)(VLSI Design)

### III Semester

Course Code	Name of the Course	Credits	Hours per Week	Continuous Evaluation	Semester End Examination	Total Marks
EPEVD321 EPEVD322 EPEVD323 EPEVD324 EPEVD325 EPEVD326	Elective-I	4	4L	40	60	100
EPEVD331 EPEVD332 EPEVD333 EPEVD334 EPEVE335	Elective –II	4	4L	40	60	100
EPEVD311	Technical Seminar I	2	4L	100	-	100
EPEVD312	Project Work	8		50	50	100
	Total	18	12	230	170	400

#### Elective-1

EPEVD321	: Digital Signal Processing
EPEVD322	: Microcontrollers and Applications
EPEVD323	: Wireless Communications and Networks
EPEVD324	: DSP Processors and Architectures
EPEVD325	: VLSI Technology
EPEVD326	: RF IC Design

#### Elective-II

EPEVD331:	Operating Systems
EPEVD332:	Advanced Computer Architecture
EPEVD333:	Image and Video Processing
EPEVD334:	Communication Networks
EPEVD335:	Advanced Digital Signal Processing

### IV Semester

Course Code	Name of the Course	Credits	Hours per Week	Continuous Evaluation	Semester End Examination	Total Marks
EPEVD411	Technical Seminar-II	2	4L	100	--	100
EPEVD412	Project work	16	-	50	50	100
	Total	18	4	150	50	200

**Total Credits 74**

## **M.Tech (Evening) VLSI Design**

The Department of Electronics and Communication Engineering, GITAM University is offering a two year Post Graduate M.Tech in VLSI as Evening programme. The classes would be conducted between 5.30p.m. to 8.30p.m. from Monday to Friday. The program is designed for employees working with various industries for improvement of their qualification and acquiring skills in VLSI Design while working full time at their respective organizations. This program will enhance the human resource required for VLSI technology industries. The curriculum for this evening program is so designed such that all the subjects / topics covered in the regular program of M.Tech VLSI Design are the same. Such a program in the evening is first of its kind to be started in the state of Andhra Pradesh. Number of students admitted during academic year 2009-10 is 10